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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/634,589	08/05/2003		Keerthinarayan P. Heragu	TI-35285 6573		
23494	7590	09/24/2004		EXAMINER		
TEXAS IN P O BOX 65		ENTS INCORPOR S 3999	TRAN, MICHAEL THANH			
DALLAS, TX 75265				ART UNIT	PAPER NUMBER	
				2818	· · · · · · · · · · · · · · · · · · ·	

DATE MAILED: 09/24/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/634,589	HERAGU ET AL.				
Office Action Summary	Examiner	Art Unit				
	Michael t Tran	2818				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 05 A	ugust 2003.					
<u> </u>	action is non-final.					
· <u> </u>	n for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
<ul> <li>4)  Claim(s) 1-20 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdraw</li> <li>5)  Claim(s) 11-20 is/are allowed.</li> <li>6)  Claim(s) 1,3,4,9 and 10 is/are rejected.</li> <li>7)  Claim(s) 2 and 5-8 is/are objected to.</li> <li>8)  Claim(s) are subject to restriction and/or</li> </ul>	wn from consideration.					
Application Papers						
9) The specification is objected to by the Examine	r.					
<u> </u>	☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correct	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)☐ The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National Stage				
Attachment(s)	$\mathcal{M}(\mathcal{I})$					
Attachment(s)  1) M Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) ☑ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail Da					
2) Information Disclosure Statement(s) (P1O-1449 or P1O/SB/08)  Paper No(s)/Mail Date <u>080503</u> .	6) Other:	aton reprioduon (i 10-102)				

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#### **DETAILED ACTION**

1. In response to the Communications dated August 05, 2003, claims 1-20 are active in this application.

#### Information Disclosure Statement

2. The information disclosure statement filed August 05, 2003 has been considered.

## Claim Objections

3. Claims 2 and 5-8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

It appears that claim 7 is incomplete.

## Claim Rejections - 35 U.S.C. § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in-
- (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or

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(2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

5. Claims 1, 3, 4, 9, and 10 are rejected under 35 U.S.C 102(b) as being anticipated by Kim et al. [U.S. Patent #6,091,663].

With respect to claim 1, Kim et al. disclose a method for timing memory access in a clocked circuit, comprising: receiving a clock pulse used as a first strobe signal to a first strobe input of a memory for initiating a first memory access; receiving a strobe ready signal from the memory after the first memory access; generating a second strobe signal in response to the strobe ready signal; and transmitting the second strobe signal to a second strobe input of the memory for initiating a second memory access during the clock pulse. See 10<sup>th</sup> paragraph of the "Summary of the Invention" section. In the cited section, Kim et al. indicated that there are two separate strobe signals for separate memory access.

With respect to claim 3, Kim et al. disclose, in figure 4, the device further comprising a constant input to at least one memory select input [WS] of the memory for allowing the first memory access and the second memory access.

With respect to claim 4, Kim et al. disclose, in figure 4, the device further comprising a constant signal to a first memory select input of the memory for allowing the first memory access.

With respect to claim 9, Kim et al. disclose that the clock pulse is from a system clock. See "Summary of the Invention" section.

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With respect to claim 10, Kim et al. disclose that the clock pulse is a positive clock pulse. See figure 8.

## Allowable Subject Matter

- 6. Claims 11-20 are allowable over the prior art of record.
- 7. The following is an Examiner's statement of reasons for the indication of allowable subject matter: the prior art of records does not show (in addition to the other elements in the claim) the following:
  - Generating a select signal in response to the clock pulse; and providing the select signal to at least one data addressing multiplexer for selecting an input for the second memory access.
  - Generating a select signal in response to the clock pulse; and providing the select signal to a second MS input of the memory and a data addressing multiplexer for allowing the second memory access.
  - Generating a select signal in response to the clock pulse; and generating the second strobe signal in response to the strobe ready signal and the select signal.
  - Generating the select signal from an output of a first flip-flop; and generating the second strobe signal from an output of a second flip-flop.
  - ❖ The strobe ready signal being generated by the memory after the first memory access is complete.

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#### **Conclusion**

8. When responding to the Office action, Applicants are advised to provide the Examiner with line and page numbers of the application and/or references cited to assist the Examiner in the prosecution of this case.

9. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Michael T. Tran whose telephone number is (571) 272-1795. The Examiner can normally be reached on Monday-Thursday from 7:30-6:00 P.M.

10. Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (571) 272-1650.

Michael T. Tran Art Unit 2818

September 22, 2004